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Compiling Dynamic Mappings with Array Copies
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Abstract

Array remappings are useful to many applications on distributed memory parallel machines. They are available in High Performance Fortran, a Fortran-based data-parallel language. This paper describes techniques to handle dynamic mappings through simple array copies: array remappings are translated into copies between statically mapped distinct versions of the array. It discusses the language restrictions required to do so. The remapping graph which captures all remapping and liveness information is presented, as well as additional data-flow optimizations that can be performed on this graph, so as to avoid useless remappings at run time. Such useless remappings appear for arrays that are not used after a remapping. Live array copies are also kept to avoid other flow-dependent useless remappings. Finally the code generation and runtime required by our scheme are discussed. These techniques are implemented in our prototype HPF compiler.

1 Introduction

Array remappings, i.e. the ability to change array mappings at runtime, are definitely useful to applications and kernels such as ADI [16], linear algebra solvers [19], 2d FFT [10], signal processing [17] or tensor computations [2] for efficient execution on distributed memory parallel computers. HPF [15, 12] provides explicit remappings through realign and redistribute directives and implicit ones at subroutine calls and returns for array arguments. This paper discusses compiler handling of remappings and associated data flow optimizations.

\begin{verbatim}
hpfs$ align with B:: C
hpfs$ distribute B(block,*)
...
! C is remapped
hpfs$ realign C(i,j) with B(j,i)
! C is remapped back to initial!
hpfs$ redistribute B(*,block)

Figure 2: useless C remappings

hpfs$ align with T :: &
hpfs$ A,B,C,D,E
hpfs$ distribute T(block)
...
A B C D E ...
hpfs$ redistribute T(cyclic)
...
A D ...

Figure 3: Aligned array remappings
\end{verbatim}

1.1 Motivation

Remappings are costly at runtime because they imply communication. Moreover, even well written HPF programs may require useless remappings. In Figure 1 the change of both alignment and distribution of A requires two remappings while it could be remapped at once from (block,* ) to (*,cyclic) rather than using the intermediate (*,block) mapping. In Figure 2 both C remappings are useless because the redistribution restores its initial mapping. In Figure 3 template T redistribution enforces the remapping of all five aligned arrays, although only two of them are used afterwards. In Figure 4 the consecutive calls to subroutine foo remap the argument on entry in and on exit from the routine, and both back and forth remappings could be avoided between the two calls. Moreover, between calls to foo and bla, array Y is remapped from (cyclic) to (block) and then from (block) to (cyclic(2)) while a direct remapping would be possible. All these examples do not arise from badly written programs, but from a normal use of HPF features. They demonstrate the need for compile-time data flow optimizations to avoid remappings at run time.
The Static Distribution Assignment scheme [18] to handle dynamic array references is very similar to our approach which uses distinct copies for each array mapping. Both schemes have been developed concurrently. Such techniques require well-behaved programs: remappings should not appear anywhere in the program, to avoid references with ambiguous mappings as shown in Figure 5. We go a step further by suggesting [4] that the language should forbid such cases. This is supported by our experience with real applications that require dynamic mappings: ambiguous mappings are rather bugs to be reported.

Our approach is unique from several points. First, our optimizations are expressed on the remapping graph which captures all mapping and use information for a routine. This graph can be seen as the dual of a contracted control-flow graph as noted in [18]. The advantage is that our graph is much smaller than the usual control-flow graph. Second, read and write uses of arrays are distinguished, enabling the detection of live copies that can be reused without communication in case of a remapping. Third, our runtime can handle arrays with an ambiguous mapping, provided that it is not referenced in such a state. This requirement is weaker than the one for well-behaved programs [18, 11], since it enables cases such as Figure 6.

1.3 Outline

This paper describes a practical approach to handle HPF remappings. All issues are addressed: languages restrictions (or corrections) required for this scheme to be applicable, actual management of simple references in the code, data-flow optimizations, down to the runtime system requirements. This technique is implemented in our HPF compiler [3].

First, Section 2 presents the language restrictions, the handling of subroutine calls and our general approach to compile remappings. Second, Section 3 focuses on the definition and construction of the remapping graph which captures all necessary remapping and liveliness information on a contracted control flow graph. Third, Section 4 discusses data-flow optimizations performed on this small graph. These optimizations remove all useless remappings and detect live or may-be-live copies to avoid further communication. Finally, Section 5 outlines runtime requirements implied by our technique, before concluding.

2 Overview

This paper focuses on compiling HPF remappings with array copies and on suggesting optimization techniques to avoid useless remappings. The idea is to translate a program with dynamic mappings into a standard HPF program with copies between differently mapped arrays, as outlined in Figure 7: the redistribution of array A is translated into a copy from A1 to A2; the array references are updated to the appropriate array version.

2.1 Language restrictions

In order to do so, the compiler must know statically about mappings associated to every array references. Thus the
The HPF language must be restricted to enable the minimum static knowledge required to apply this scheme. Namely:

1. References with ambiguous mappings due to the control-flow of the program are forbidden. Hence the compiler can figure out the mapping of array references and substitute the right copy.

2. Interfaces describing mappings of arguments of called subroutines are mandatory. Thus all necessary information is available for the caller to comply to the argument mapping of its callees.

3. Transcriptive mappings associated to subroutine arguments are forbidden. This feature can be replaced by a more precise mapping descriptions [6], or could be enabled but would then require an interprocedural compilation such as cloning [18].

Condition 1 is illustrated in Figure 5: Array A mapping is modified by the redistribute if the realign was executed before at runtime, otherwise A is aligned with template T1 and get through T2 redistribution unchanged. However there may be an ambiguity at a point in the program if the array is not referenced: in Figure 6 after the endif and before the final redistribution the compiler cannot know whether array A is distributed block or cyclic, but the mapping ambiguity is solved before any reference to A.

With these language restrictions the benefit of remappings is limited to software engineering issues since it is equivalent to a static HPF program. It may also be argued that expressiveness is lost by restricting the language. However it must be noted that: (1) software engineering is an issue that deserves consideration; (2) the current status of the language definition is to drop remappings as a whole (by moving them out of the core language as simple approved extensions [12] because they are considered too difficult to handle; (3) we have not encountered any real application so far that would benefit from the full expressiveness of arbitrary flow-dependent remappings. Thus it makes sense to keep the simple and interesting aspects of remappings. Further powerful extensions can be delayed until applications need them and when compilation techniques are proven practical and efficient.

These language restrictions are also required to compile remappings rather than to rely on generic library functions. Indeed, for compiling a remapping into a message passing SPMD code [5] both source and target mappings must be known. Then the compiler can take advantage of all available information to generate efficient code. The implicit philosophy is that the compiler handles most of the issues at compile time, with minimum left to run time. But the language must require the user to provide the necessary information to the compiler. If not, only runtime-oriented approaches are possible, reducing the implementor’s choices, but also performances.

2.2 Subroutine arguments

Subroutine argument mappings will be handled as local remappings by the caller. This is possible if the caller knows about the mapping required by callee dummy arguments, hence the above constraint to require interfaces describing argument mappings. The intent attribute (in, out or inout) provides additional information about the effects of the call onto the array. It will be used to determine live copies over call sites without interprocedural techniques.

Subroutine calls are translated as explicit remappings in the caller as suggested in Figure 8. Our scheme respects the intended semantics of HPF argument passing: the argument is the only information the callee obtains from the caller. Thus explicit remappings of arguments within the
callee will only affect copies local to the subroutine. Under more advance calling conventions, it may be thought of passing live copies along the required copy, so as to avoid further useless remappings within the subroutine.

2.3 Discussion

The current HPF specification includes features (inherit directive for transcripative mappings, possible ambiguous mappings, etc.) that make the runtime approach mandatory, at least for handling all cases. Another side-effect of optional interfaces, transcripative mappings and weak descriptive mappings is that the compiler must make the callee handle remappings as a default case. But the callee has both less information and optimization opportunities [11].

These features improve expressiveness, but at the price of performance. Delaying to run time the array mapping handling of references means delaying the actual address calculations and reduces compile time optimizations which are mandatory to cache-based processors. Also compiling for an unknown mapping makes many communication optimizations impractical. Expensive and more complex techniques can be used to generate good code when lacking information: partial or full cloning of subroutines to be compiled with different assumptions, that requires a full interprocedural analysis and compilation [18]. Another technique is run time partial evaluation that dynamically generates an optimized code once enough information is available [7]. However even though there are overheads and the runtime is complex.

As HPF is expected to bring high performance, transcripative and ambiguous mappings seem useless. They restrict the implementator choices and possible optimizations. Moreover no real-life application we have encountered so far require them to reach high performance levels.

3 Remapping graph $G_R$

This section defines and describes the construction of the remapping graph. This graph is a subgraph of the control flow graph which captures remapping information such as the source and target copies for each remapping of an array and how the array is used afterwards, that is a liveliness information. Subsequent optimizations will be expressed on this small graph.

3.1 Definition

In the following we will distinguish the abstract array and its possible instances with an associated mapping. Arrays are denoted by capital typewriter letters as $A$. Mapped arrays are associated a subscript such as $A_k$. Differently subscripted arrays refer to differently mapped instances.

The remapping graph is a very small subgraph of the control flow graph. The vertices of the graph are the remapping statements whether explicit or added to model implicit remappings at call sites. There is a subroutine entry point vertex $v_0$ and an exit point $v_e$. An edge denotes a possible path in the control flow graph with the same array remapped at both vertices. The vertices are labeled with the remapped arrays. Each remapped array is associated one leaving copy and reaching copies at this vertex. Arrays are also associated a conservative use-information: Namely whether a given leaving copy may be not referenced (W), fully redefined before any use (D), only read (R) or maybe modified (M).

Figure 9 shows a label representation. Array $A$ remapping links reaching copies $\{1,3\}$ to the leaving mapping 2, the new copy being only read (R). The vertex is a remapping for array $A$. It may be reached with copies $V_k$ and $V_0$ and must be left with copy $V_e$. As this copy will only be read, the compiler and runtime can decide to keep the reaching copy values which are live. A shorthand is used in some figures when several arrays share the same reaching and leaving mappings: All concerned arrays are specified as a prefix, and the use information over the arrow is specified for each array, respectively.

This provides a precise liveliness information that will be used by the runtime and other optimizations to avoid remappings by detecting and keeping live copies. However it must be noted that this information is conservative, because abstracted at the high remapping graph level. The collected information can differ from the actual runtime effects on the subroutine: an array can be qualified as W in a point and not be actually modified. The remapping graph definition is more formally presented in Appendix A.

3.2 Construction

The remapping graph described above holds all the remapping and liveliness information. The next issue is to build this graph. The construction algorithm builds the remapping graph and updates the control graph to (1) switch array references to the appropriate copy, distributed as expressed by the program, (2) reflect implicit remappings of array arguments through explicit remappings and (3) check the conditions required for the correctness of our scheme.

Subroutine argument mappings are handled as local remappings by the caller. Implicit remappings are translated into explicit ones at call site in the caller. The actual array argument is copied if needed into a copy mapped as the corresponding dummy argument before the call, and may be copied back on return. The intent attribute (in, out or inout) provides information about the effects of the call onto the array and will be used to determine live copies. Within the subroutine compilation, three added vertices (call $v_c$, entry $v_e$ and exit $v_o$) model the initial and final mappings for the dummy arguments and local variables. Dummy arguments and local arrays are associated their initial mapping on exit from vertex $v_e$, $v_o$ and $v_c$ allow to attach dummy arguments the use information derived from the intent attribute to model imported and exported values.

Then the construction starts by propagating the initial mapping copy of the array from the entry point of the subroutine. The $G_R$ construction algorithm pushes array versions along the control graph and extract a simpler graph to reflect the needed runtime copies to comply to the intended semantics of the program. This construction can be described as a set of data-flow problems detailed in Appendix B. Mappings are propagated from the entry point and updated at remapping statements. This can be decomposed into two dataflow problems, one for alignments and one for distributions. However our implementation performs both propagation concurrently, focussing directly on
array mappings. The propagation tags array references with their associated mappings and performs some transformations to handle subroutine calls. Second, the use information is propagated backwards from references to remapping statements. Finally the contracted graph is defined by propagating remapping statements over the control graph.

3.3 Example

Let us focus on the routine in Figure 10. It contains four remappings, thus with the added call, entry and exit vertices there are seven vertices in the corresponding remapping graph. There are three arrays, two of which are local. The sequential loop structure with two remappings is typical of ADI.

Figure 11 shows the resulting remapping graph. The liveliness information is represented above the arrow. The rationale for the 1 to E and 2 to E edges is that the loop nest may have no iteration at runtime, thus the remappings within the array may be skipped. Since all arrays are aligned together, they are all affected by the remapping statements. Four different versions of each array might be needed with respect to the required different mapping. However, the liveliness analysis shows that some instances are never referenced such as B3 and C2.

4 Data flow optimizations

The remapping graph $G_R$ constructed above abstracts all the liveliness and remapping information extracted from the control flow graph and the required dynamic mapping specifications. Following [11] we plan to exploit as much as possible this information to remove useless remappings that can be detected at compile time, or even some that may occur under particular run time conditions. These optimizations on $G_R$ are expressed as standard data flow problems [14, 13, 1].

4.1 Removing useless remappings

Leaving copies that are not live appear in $G_R$ with the $\mathrm{N}$ (not used) label. It means that although some remapping on an array was required by the user, this array is not referenced afterwards in its new mapping. Thus the copy update is not needed and can be skipped. However, by doing so, the set of copies that may reach latter vertices is changed. Indeed, the whole set of reaching mappings must be recomputed. It is required to update this set because we plan a compilation of remappings, thus the compiler must know all possible source and target mapping couples that may occur at run time. This recomputation is a may forward standard data flow problem. It is detailed in appendix C. First useless remappings are removed (unused leaving mappings). Second reaching mappings are computed again from remaining leaving mappings. This optimization is shown correct: All remapping that are useless are removed, and all those that may be useful are kept. Thus it is optimal, provided that remappings remain in place.

Figure 12 displays the remapping graph of our example after optimization. From the graph it results that array A may be used with all possible mappings $\{0, 1, 2, 3\}$, but array B is only used with $\{0, 1\}$ and array C with $\{0, 3\}$. Array C is not live but within the loop nest, thus its instantiation can be delayed, and may never occur if the loop body is never executed. Array B is only used at the beginning of the program, hence all copies can be deleted before the loop.

The generation of the code from this graph is detailed in section 5.

4.2 Dynamic live copies

Through the remapping graph construction algorithm, array references in the control graph $G_C$ were updated to an array version with a statically known mapping. The remapping graph holds the information necessary to organize the copies between these versions in order to respect the intended semantics of the program. The first idea is to allocate the leaving array version when required, to perform the copy and to deallocate the reaching version afterwards.

However, some copies could be kept so as to avoid useless remappings when copying back to one of these copies if the array was only read in between. The remapping graph holds the necessary information for such a technique. Let us consider the example in Figure 13 and its corresponding remapping graph in Figure 14. Array A is remapped differently in the branches of the condition. It may be only modified in the then branch. Thus, depending on the execution path in the program, array copy $A_0$ may reach remapping statement 3 live or not. In order to catch such cases, the liveliness management is delayed until run time: dead copies will be deleted (or mark as dead) at the remapping statements.

Keeping array copies so as to avoid remappings is a nice but expensive optimization, because of the required memory. Thus it would be interesting to keep only copies that may be used latter on. In the example above it is useless to keep copies $A_1$ or $A_2$ after remapping statement 3 because the array will never be remapped to one of these distributions. Determining at each vertex the set of copies that may be live and used latter on is a may backward standard data flow problem: leaving copies must be propagated backward on paths where they are only read. This is detailed in Appendix D.

4.3 Other optimizations

Further optimization can be thought of, as discussed in [11]. Array kill analysis, for instance based on array regions [8, 9], tells whether the values of an array are dead at a given point in the program. This semantical analysis can be used to
subroutine remap(A,m)
  parameter(n=1000)
  intent(inout):: A
  real, dimension(n,n):: A,B,C
!hpf$ align with A:: B,C
!hpf$ distribute A(block*)
... B written, A read
if (...) then
!hpf$ redistribute A(cyclic*)
... A p written, A B read
else
!hpf$ redistribute A(block,block)
... p written, A read
endif
do i=1, m+p
!hpf$ redistribute A(cyclic/)
1 A p written, A read
!hpf$ redistribute A(cyclic/*)
2 A written
else
!hpf$ redistribute A(cyclic(2)/)
2 A written
endif
!hpf$ redistribute A(block)
3 A read enddo
end subroutine remap

Figure 10: Code example

!hpf$ distribute A(block)
→ 0
... A read
if (...) then
!hpf$ redistribute A(cyclic)
→ 1
... A written
else
!hpf$ redistribute A(cyclic(2))
→ 2
... A read
endif
!hpf$ redistribute A(block)
→ 3
... A read
end

Figure 13: Flow dependent live copy

5 Runtime issues

The remapping graph information describing array versions reaching and leaving remapping vertices must be embedded into the program through actual copies in order to fulfill the requirements. Some optimizations described in the previous sections rely on the runtime to be performed.

avoid remapping communication of values that will never be reused. Array regions can also describe a subset of values which are live, thus the remapping communication could be restricted to these values, reducing communication costs further. However such compile-time advanced semantical analyses are not the common lot of commercial compilers. Our prototype HPF compiler includes a directive for the user to provide this information. The directive creates a remapping vertex tagged D.

Remappings can be moved around in the control flow graph, especially out of loops. From the code in Figure 15 we suggest to move the remappings as shown in Figure 16. This differs from [11]: the initial remapping is not moved out of the loop, because if t < 1 this would induce a useless remapping. The remapping from block to cyclic will only occur at the first iteration of the loop. At others, the runtime will notice that the array is already mapped as required just by an inexpensive check of its status.

5.1 Runtime status

Some data structure must be managed at run time to store the needed information: namely, the current status of the array (which array version is the current one and may be referenced) and the live copies.

The current status of an array can be kept in a descriptor holding the version number. By testing this status, the runtime is able to notice which version of an array reaches a remapping statement, what may be flow-dependent. This
The algorithm for generating the copy update code and liveness information from the remapping graph is outlined in Figure 19. Copy allocation and deallocation are inserted in the control flow graph to perform the required remappings, using the sets computed at the $G_R$ optimization phase.

The first loop inserts the runtime management initialization at the entry point. All copies are denoted as not live. No copy receives an a priori instantiation. The rationale for doing so is to delay this instantiation to the actual use of the array, which may occur with a different mapping or never, as Array C in Figure 10. The second loop nest extracts from the remapping graph the required copy for all vertex and all remapped arrays, if there is some leaving mapping for this array at this point. Copies that were live before but that are not live any more are cleaned, i.e., both freed and marked as dead. Finally a full cleaning of local arrays is inserted at the exit vertex. Figure 20 shows a generated copy code for

Figure 15: Loop invariant remappings

```
! hpfs distribute A(block) ...
! hpfs redistribute A(cyclic) ...
!
endo

Figure 16: Optimized version

for A \in S(v_0)
    append to v_0 "status(A)\=true"
for a \in C(A)
    append to v_0 "live(A_a)\=false"
end for

for v \in V(G_R) - \{v_i\}
for A \in S(v)
    if (L_A(v) \neq L_A(u))
        append to v "if (status(A)\=false)" allocate A
        append to v "if (not live(A_a)) if needed" free A
        append to v "if (status(A)\=true) A_a\=true"
        end if
    end if
end for

for all A
    for v \in C(A)
        append to v_a "if (live(A_a) and needed) free A_a"
    end for
end for
```

Figure 19: Copy code generation

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Figure 18: Mapping restored

```
! save the reaching status
reaching[\#1]=status[\#1]
!
!
! redistribute A(block)
call foo(A)
!
!
! redistribute A(cyclic)
!
!
enddo
```

```
!
!
enddo

```

Figure 20: Code for Figure 9
the remapping vertex in Figure 9.

It must be noted that dead arrays (D) do not require any actual array copy, thus none is generated, avoiding communication at run time. Moreover, there is no initial mapping imposed from entry in the subroutine. If an array is remapped before any use, it will be instantiated at the first remapping statement encountered at runtime with a non empty leaving copy. Finally, care must be taken not to free the array dummy argument copy which belongs to the caller.

Another benefit from this dynamic live mapping management is that the runtime can decide to free a live copy if not enough memory is available, and to change the corresponding liveness status. If required latter on, the copy will be regenerated, i.e. both allocated and properly initialized with communication. Since the generated code does not assume that any live copy must reach a point in the program, but rather decided at remapping statements what can be done, the code for the communication will be available.

6 Conclusion

In this paper, we have shown a practical approach to compile HPF dynamic mappings. It consists of substituting dynamic arrays by static ones, and of inserting simple array copies between these arrays when necessary. Implicit remappings at call site are translated into explicit ones in the caller. We have discussed the language restrictions needed to apply this scheme, and argued that no high performance application should miss the restricted features. We have also presented optimizations enabled by our technique, to remove useless remappings and to detect live copies that can be reused without communication. Finally runtime implications have been discussed.

Most of the techniques described in this paper are implemented in our prototype HPF compiler [3]. It is available from http://www.cri.enmp.fr/pip/hpfc.html. The standard statically mapped HPF code generated is then compiled, with a special code generation phase for handling remapping communication due to the explicit array copies.

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References


Each edge is labelled with the arrays that are remapped from at the sink vertex when coming from the source vertex: $A(v, v')$. Note that

\[ A \in A(v, v') \Rightarrow A \in S(v) \text{ and } A \in S(v') \]

## B Remapping Graph Construction

Here is a data flow formulation of the construction algorithm. First, let us define the sets that will be computed by the dataflow algorithms in order to build $\mathcal{G}_R$:

- **REACHING($v$)**: the set of arrays and associated mappings reaching vertex $v$: these arrays may be remapped at the vertex or left unchanged, thus going through the vertex.

- **LEAVING($v$)**: the set of arrays and associated mappings leaving vertex $v$: one leaving mapping per array is assumed for simplifying the presentation.

- **REMAPMED($v$)**: the set of arrays actually remapped at vertex $v$. (note that if several leaving array mappings are allowed, this information is associated to array and mapping couples instead of just considering arrays).

- **EFFECTSOF($v$)**: the proper effect on distributed variables of vertex $v$, i.e., these variables and whether they are never referenced, fully redefined, partially defined or used. This basic information is assumed to be available.

- **EFFECTSAFTER($v$)**: the distributed variables and associated effects that may be encountered after $v$ and before any remapping of these variables.

- **EFFECTSFROM($v$)**: just the same, but including also the effects of $v$.

- **REMAPMEDAFTER($v$)**: the distributed variables and associated remapping vertices that may be encountered directly (without intermediate remapping) after $v$.

- **REMAPMEDFROM($v$)**: just the same, but including also $v$.

The following function computes the leaving mapping from a reaching mapping at a given vertex:

\[ A_j = \text{IMPACT}(A_i, v) \]

\[ A_j = \text{IMPACT}(A_i, v) \]

\[ A_j = \text{IMPACT}(A_i, v) \]

\[ \text{operator} \rightarrow \text{ means } \text{but those concerning}, \text{ that is the operator is not necessarily used with sets of the same type.} \]

Now, here is the construction algorithm expressed as a set of data flow equations.
Input to the construction algorithm
- control flow graph $G_C$ with entry $v_0$ and exit $v_*$ vertices
- the set of remapping vertices $V_R$, which includes Vertex $v_0$ and Vertex $v_*$
- the proper effects of vertices on distributed variables $\text{EFFECTSOF}(v)$ (the default for $V_R$ is no effects).
- for any remapped vertex at a vertex, there is only one possible leaving mapping. This assumption simplifies the presentation, but could be removed by associating remapped information to array mappings instead of the array.

Updating $G_C$ (arguments)
First let us update $G_C$ to model the desired mapping of arguments.

- Add call vertex $v_r$ and an edge from $v_r$ to $v_0$ in $G_C$.

Reaching and Leaving mappings
They are computed starting from the entry point in the program. Propagated mappings are modified by remapping statements as modeled by the $\text{IMPACT}$ function, leading to new array versions to be propagated along $G_C$. This propagation is a may forward dataflow problem.

Initialization:
- $\text{REACHING} = \emptyset$
- $\text{LEAVING} = \emptyset$
- add all argument distributed variables and their associated mappings to $\text{LEAVING}(v_r)$ and $\text{LEAVING}(v_*)$.
- update $\text{EFFECTSOF}(v_r)$ and $\text{EFFECTSOF}(v_*)$ as suggested in Figure 22. If values are imported the array is annotated as defined before the entry point. If values are exported, it is annotated as used after exit. This models safely the caller context. The callee is assumed to comply to the intended semantics.

\begin{table}[h]
\centering
\begin{tabular}{|c|c|c|}
\hline
intent & $U_k(v_r)$ & $U_k(v_*)$ \\
\hline
in & D & H \\
\hline
out & D & W \\
\hline
\end{tabular}
\caption{Array argument use}
\end{table}

\begin{figure}
\centering
\begin{tikzpicture}
\node (v0) at (0,0) [circle,draw] {};
\node (v1) at (1,0) [circle,draw] {};
\node (v2) at (2,0) [circle,draw] {};
\draw (v0) -- (v1);
\draw (v1) -- (v2);
\end{tikzpicture}
\caption{Initial $G_R$}
\end{figure}

\begin{figure}
\centering
\begin{tikzpicture}
\node (v0) at (0,0) [circle,draw] {};
\node (v1) at (1,0) [circle,draw] {};
\node (v2) at (2,0) [circle,draw] {};
\draw (v1) -- (v2);
\end{tikzpicture}
\caption{Array argument use}
\end{figure}

\begin{figure}
\centering
\begin{tikzpicture}
\node (v0) at (0,0) [circle,draw] {};
\node (v1) at (1,0) [circle,draw] {};
\node (v2) at (2,0) [circle,draw] {};
\draw (v0) -- (v1);
\draw (v1) -- (v2);
\end{tikzpicture}
\caption{Call with a prescriptive inout-intended argument}
\end{figure}

\begin{itemize}
\item add all local distributed variables and their associated initial mapping to $\text{LEAVING}(v_0)$.
\item the array mappings reaching a vertex are those leaving its predecessors.
\item the array mappings leaving a vertex are updated with the statement impact on the array mappings reaching this vertex.
\item note that there may be none if some piece of code is dead.
\end{itemize}

Updating references
For all vertices $v \in \mathcal{V}(G_C) - V_R$ so that $\text{EFFECTSOF}(v)$ on is not $H$:
- if $|\{m \in \text{LEAVING}(v), \text{ARRAY}(m) = A\}| > 1$ then issues an error, because there is more than one mapping for a given array
- else substitute the references with the corresponding array copy.

Remapped arrays
They are directly extracted from $\text{REACHING}$; they are those transformed by $\text{IMPACT}$.

\begin{eqnarray*}
\text{REMAPPE}(v) &=& \bigcup_{m \in \text{REACHING}(v) \cap \text{IMPACT}(m,v)} \text{ARRAY}(m)
\end{eqnarray*}

\begin{figure}
\centering
\begin{tikzpicture}
\node (v0) at (0,0) [circle,draw] {};
\node (v1) at (1,0) [circle,draw] {};
\node (v2) at (2,0) [circle,draw] {};
\draw (v0) -- (v1);
\draw (v1) -- (v2);
\end{tikzpicture}
\caption{Initial remapping graph with an inout intended array argument $A$ and a local array $L$.}
\end{figure}

\begin{figure}
\centering
\begin{tikzpicture}
\node (v0) at (0,0) [circle,draw] {};
\node (v1) at (1,0) [circle,draw] {};
\node (v2) at (2,0) [circle,draw] {};
\draw (v0) -- (v1);
\draw (v1) -- (v2);
\end{tikzpicture}
\caption{Call with a prescriptive inout-intended argument}
\end{figure}

\begin{itemize}
\item calls with distributed arguments are managed as shown in Figure 24:
\item $\text{pred}(v_b) = \text{pred}(v), \text{succ}(v_b) = \{v\}, \text{pred}(v_a) = \{v\}, \text{succ}(v_a) = \{v\}$
\item $\text{REMAPPE}(v_b) = \{A\}$
\item $V_R$ is updated accordingly: $V_R = V_R \cup \{v_b, v_a\}$
\end{itemize}
Summarizing effects

This phase summarizes the use information after remapping statements, and up to any other remapping statement. Hence it captures what may be done with the considered array copy.

This phase is based on proper effects that are directly extracted from the source code for direct references, or through intent declarations in subroutine explicit interfaces. Depending on the intent attribute associated to a subroutine argument the corresponding effect is described in Figure 25.

<table>
<thead>
<tr>
<th>intent</th>
<th>effect</th>
</tr>
</thead>
<tbody>
<tr>
<td>in</td>
<td>R</td>
</tr>
<tr>
<td>inout</td>
<td>W</td>
</tr>
<tr>
<td>out</td>
<td>D</td>
</tr>
</tbody>
</table>

Figure 25: Intent effect

Remapping statements but \( v_i \) and \( v_s \) have no proper effects:

\[ \forall v \in V_R - \{ v_i, v_s \}, \text{EffectsOf}(v) = \emptyset \]

This is a may backwards dataflow problem.

**Initialization:** no effects!
- \( \text{EffectsAfter} = \emptyset \)
- \( \text{EffectsFrom} = \emptyset \)

**Propagation:**
- the effects leaving a vertex are those from its successors.
  \[ \text{EffectsAfter}(v) = \bigcup_{v' \in \text{succ}(v)} \text{EffectsFrom}(v') \]
- the effects from a vertex are those leaving the vertex and proper to the vertex, but remapped arrays.
  \[ \text{EffectsFrom}(v) = (\text{EffectsAfter}(v) \cup \text{EffectsOf}(v)) - \text{Remapped}(v) \]

**Generating \( G_R \)**

From these sets we can derive the remapping graph:
- \( V_R \) are \( G_R \) vertices
- edges and labels are deduced from \( \text{RemappedAfter} \)
- \( S(), R() \) and \( L() \) from \( \text{Remapped, Reaching and Leaving} \)
- \( U() \) from \( \text{EffectsAfter} \)

**Discussion**

All the computations are simple standard data flow problems, but the reaching and leaving mapping propagation. Indeed, the \( \text{IMPACT} \) function may create new array mappings to be propagated from the vertex. The worst case complexity of the propagation and remapping graph algorithm described above can be computed. Let us denote \( n \) is the number of vertices in \( G_R \), \( s \) the maximum number of predecessors or successors of a vertex in \( G_R \), \( m \) the number of remapping statements (including the entry and exit points), \( p \) the number of distributed arrays. With the simplifying assumption that only one mapping may leave a remapping vertex, then the maximum number of mappings to propagate is \( mp \). Each of these may have to be propagated through at most \( n \) vertices with a \( \mathcal{O}(nmp^2) \) worst case complexity for a basic implementation of the union operations. Thus we can bound the worst case complexity of the propagation to \( \mathcal{O}(n sm^2 p^3) \).

**Removing useless remappings**

Leaving copies that are not live appear in \( G_R \) with the \& (not used) label. It means that although some remapping on an array was required by the user, this array is not referenced afterwards. Thus the copy update is not needed and can be skipped. However, by doing so, the set of copies that may reach latter vertices is changed. Indeed, the whole set of reaching mappings must be recomputed. It is required to update this set because we plan a compilation of remappings, thus the compiler must know all possible source and target mapping couples that may occur at run time. This recomputation is a may forward standard data-flow problem.
Remove useless remappings

Done simply by deleting the leaving mapping of such arrays.

\[ \forall v \in V(G_R), \forall A \in S(v), U_k(v) = \emptyset \Rightarrow L_k(v) = \bot \]

Recompute reaching mappings

**Initialization:** use 1-step reaching mappings

\[ R_k(v) = \bigcup_{\forall s' \in \text{pred}(v)} L_k(v') \]

Reaching mappings at a vertex are initialized as the leaving mappings of its predecessors which are actually referenced.

**Propagation:** optimizing function

\[ R_k(v) = R_k(v) \cup \bigcup_{\forall s' \in \text{pred}(v)} R_k(v') \]

The function propagates reaching mappings along paths on which the array is not referenced, computing the transitive closure of mappings on those paths.

The iterative resolution of the optimizing function is increasing and bounded, thus it converges.

Let us assume \( O(1) \) basic set element operations (put, get and membership). Let \( m \) be the number of vertices in \( G_R \), \( p \) the number of distributed arrays, \( q \) the maximum number of different mappings for an array and \( r \) the maximum number of predecessors for a vertex. Then the worst case time complexity of the optimization, for a simple iterative implementation, is \( O(m^2qr) \). Note that \( m, q \) and \( r \) are expected to be very small.

**Correctness and Optimality**

This optimization is correct and the result is optimal:

**Theorem 1** The computed remappings (from new reaching to remaining leaving) are those and only those that are needed (according to the static information provided by the data flow graph):

\[ \forall v \in V(G_R), \forall A \in S(v), U_k(v) \subseteq R_k(v), \exists v' \text{ and a path from } v' \text{ to } v \text{ in } G_R, \]

so that \( a \in L_k(v') \) and \( a \) is not used on the path.

Proof sketch: construction of the path by induction on the solution of the data flow problem. Note that the path in \( G_R \) reflects an underlying path in the control flow graph with no use and no remapping of the array.